

FEATURES	APPLICATIONS
<ul style="list-style-type: none"> - ±20ppm (Frequency Stability) Available - Low Phase Noise and Jitter Performance - RoHS Compliant - Tape and Reel 	<ul style="list-style-type: none"> - High Definition TV - Avionics - Low Phase Signal Sources - Test and Measurement Equipment



PART NUMBERING GUIDE

SUNTSU VCXO → SVC F6 C 3 A 48 B 2 - 27.000M ← **FREQUENCY (MHz)**

FR4 PCB 6 PAD →

LVDS →

SUPPLY VOLTAGE
3: 3.3V±5%
5: 5.0V±5%

FREQUENCY STABILITY
A: ±50ppm
B: ±30ppm
C: ±25ppm
*D: ±20ppm

TRI-STATE (ENABLE/ DISABLE)
BLANK: NO CONNECTION
2: Pin 2

PULLABILITY
B: ±100ppm
C: ±50ppm

OPERATING TEMPERATURE RANGE
07: 0°C to +70°C
16: -10°C to +60°C
17: -10°C to +70°C
27: -20°C to +70°C
38: -30°C to +85°C
48: -40°C to +85°C

Cage Code: 4GUT4
To customize your parameters contact a Suntsu representative.
* For frequency stability option D contact a Suntsu representative.

ELECTRICAL PARAMETERS	UNITS	MIN.	TYP.	MAX.	REMARKS
Frequency Range	MHz	10		800	
Frequency Stability (Includes Initial Tolerance at 25°C, Frequency Stability over Operating Temperature, Output Load Change, Supply Voltage Change, and First Year Aging at 25°C.)	ppm	-25		+25	See part numbering guide for options.
Operating Temperature	°C	-40		+85	See part numbering guide for options.
Storage Temperature	°C	-45		+90	
Supply Voltage (V _{DD})	3.3V Option	3.135	3.3	3.465	
	5.0V Option	4.750	5.0	5.250	
Current (I _{DD})	3.3V Option			70	
	5.0V Option			100	
Control Voltage (V _C)	3.3V Option	0.15		3.15	
	5.0V Option	0.0		5.0	
Pullability	ppm	±50		±100	See part numbering guide for options.
Input Impedance	kΩ			51	
Modulation Bandwidth	kHz	10			@-3dB.
Linearity	%			10	
Output Load (LVDS)	Ω			100	
Output Logic Levels	Output Logic High (V _{OH})		1.43	1.6	
	Output Logic Low (V _{OL})		0.9	1.1	
Differential Output Voltage (V _{OD})	mV	247	330	454	
Differential Output Error (ρV _{OD})	mV			50	
Offset Voltage (V _{OS})	V	1.125	1.250	1.375	
Offset Error (ρV _{OS})	mV			50	
Rise (T _R) and Fall (T _F) Time	ns			1	Measured at 20% to 80% of Waveform.
Tri-State Input Voltage	Enable	0.7*V _{DD}			No Connection.
	Disable			0.3*V _{DD}	
Symmetry (Duty Cycle)	%	45	50	55	
Start-Up Time	ms			10	
Phase Jitter (12kHz ~ 20MHz)	ps			1	

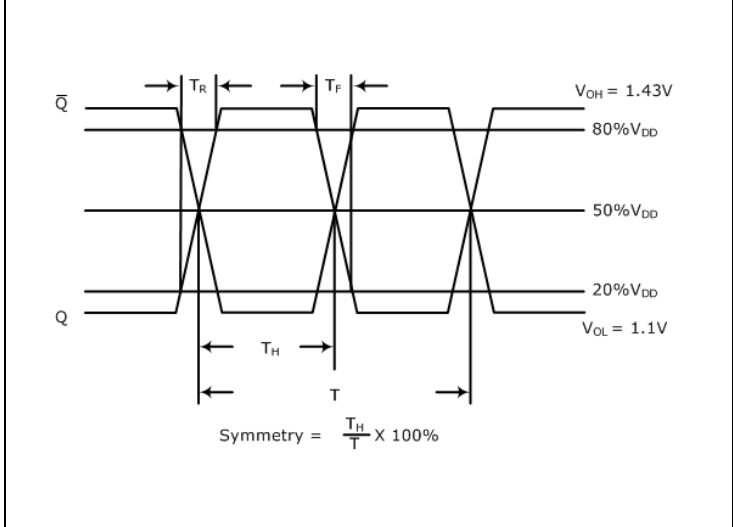
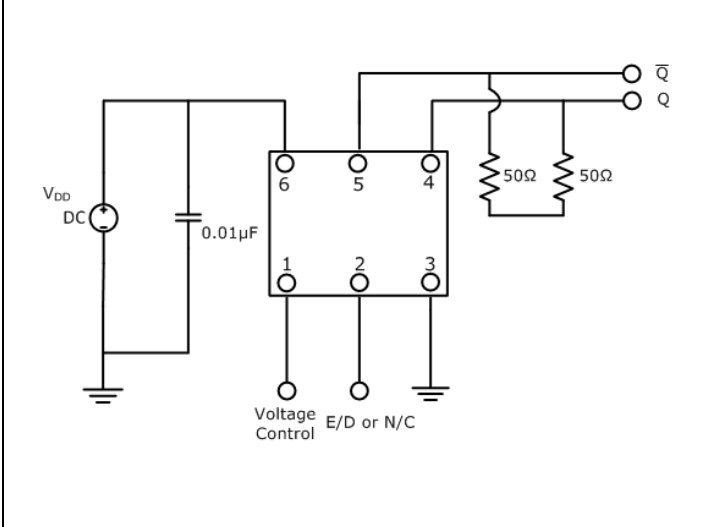
OUTLINE DRAWING

RECOMMENDED LAND PATTERN

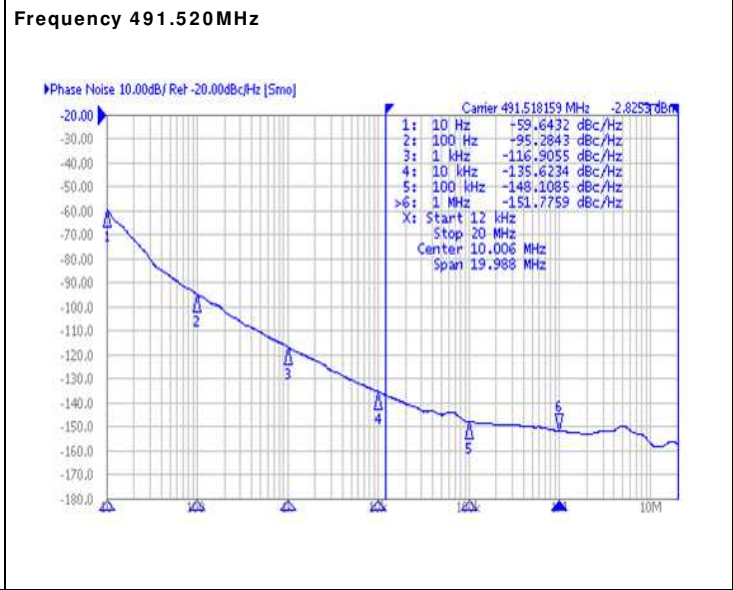
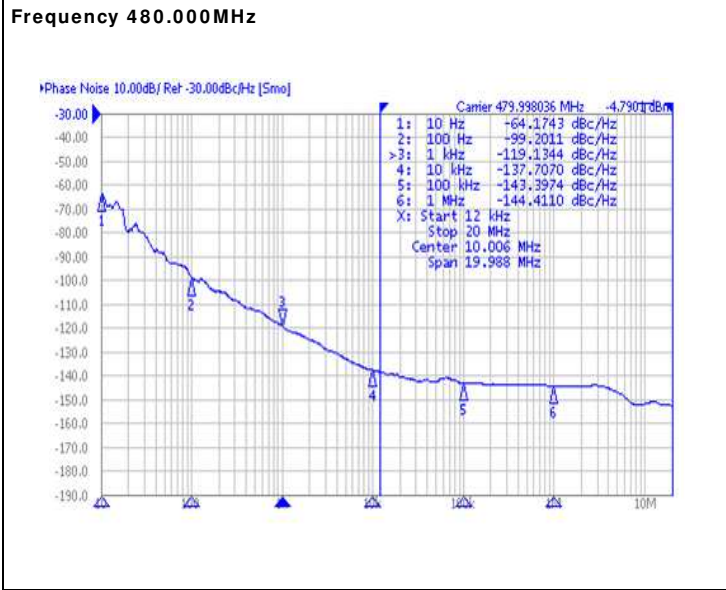
PIN	FUNCTION
1	VOLTAGE CONTROL
2	E/D or NC
3	GND
4	OUTPUT
5	COMP OUTPUT
6	V _{DD}

NOTE: Dimensions in millimeters (mm).

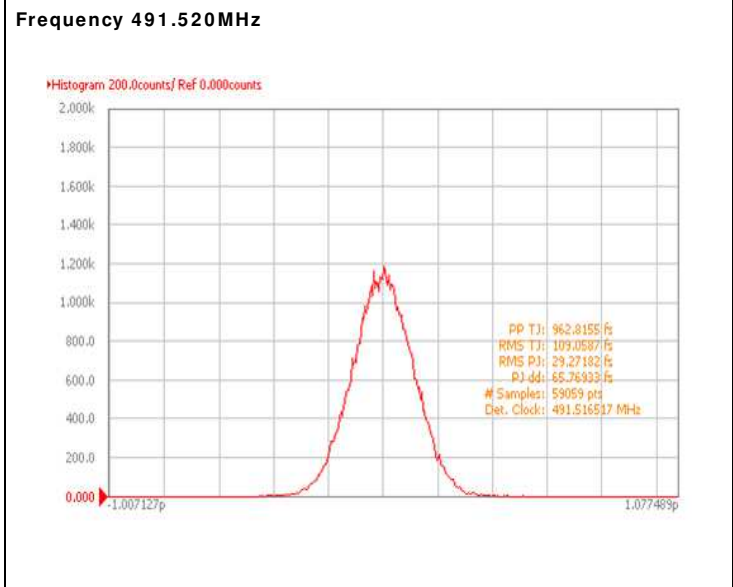
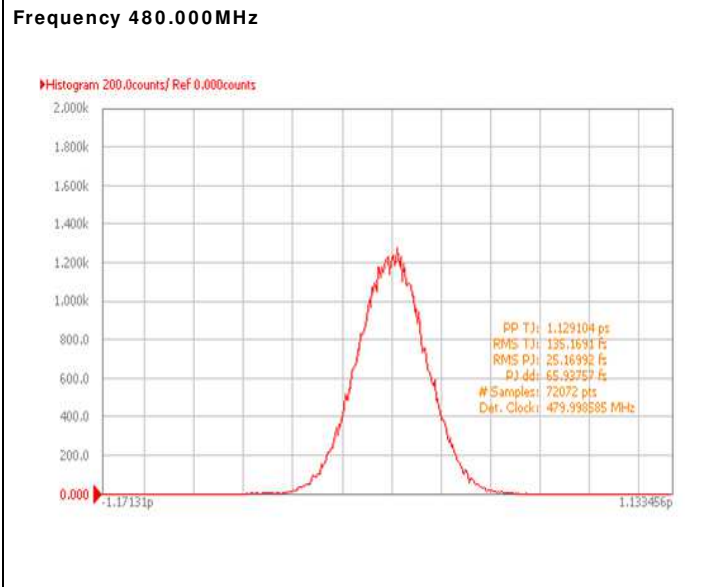
TEST CIRCUIT (LVDS) WAVEFORM (LVDS)



TYPICAL PHASE NOISE PERFORMANCE (MEASURED BY AGI LENT E5052A)



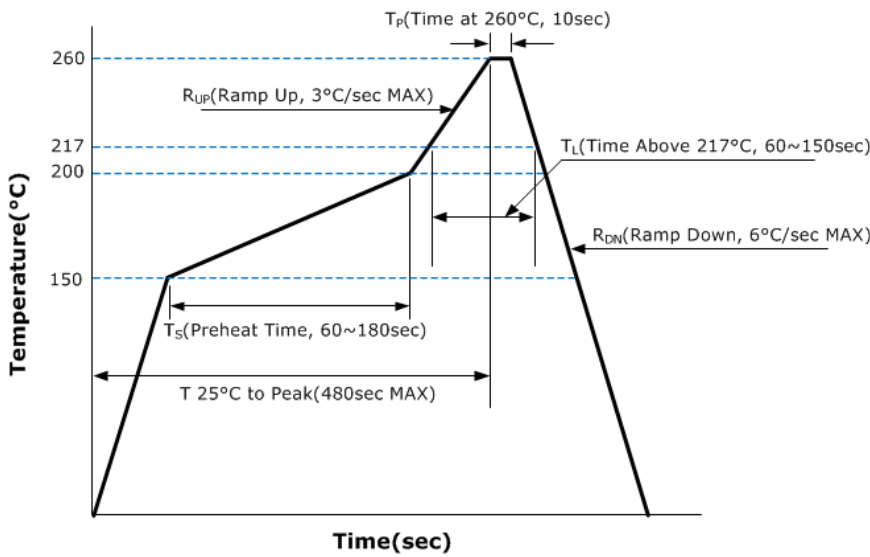
TYPICAL JITTER PERFORMANCE (MEASURED BY AGI LENT E5052A)



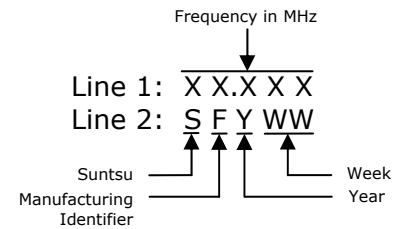
ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003

REFLOW PROFILE

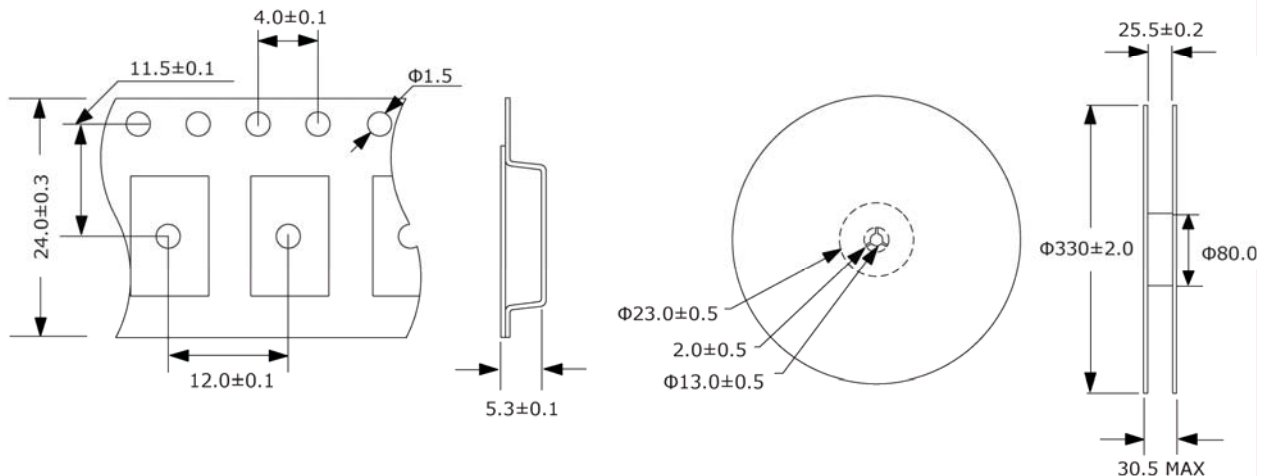


MARKING



TAPE AND REEL DIMENSIONS

500pcs/reel



NOTE: Dimensions in millimeters (mm); drawing is not to scale.