

FEATURES	APPLICATIONS
<ul style="list-style-type: none"> ±20ppm (Frequency Stability) Available Standard Full Size Package CMOS/TTL Compatible RoHS Compliant Fundamental or PLL (Phase Lock Loop) Available 	<ul style="list-style-type: none"> Phase Locked Loops Circuit Synthesizers Base Stations



PART NUMBERING GUIDE

SUNTSU VCXO → **SVC FS C 3 A 48 A - 40.000M** ← **FREQUENCY (MHz)**

FULL SIZE	↑	PULLABILITY
CMOS	↑	A: ±150ppm
SUPPLY VOLTAGE	↑	B: ±100ppm
3: 3.3V±5%	↑	C: ±50ppm
5: 5.0V±5%	↑	OPERATING TEMPERATURE RANGE
FREQUENCY STABILITY	↑	07: 0°C to +70°C
A: ±50ppm	↑	16: -10°C to +60°C
B: ±30ppm	↑	17: -10°C to +70°C
C: ±25ppm	↑	27: -20°C to +70°C
*D: ±20ppm	↑	38: -30°C to +85°C
	↑	48: -40°C to +85°C

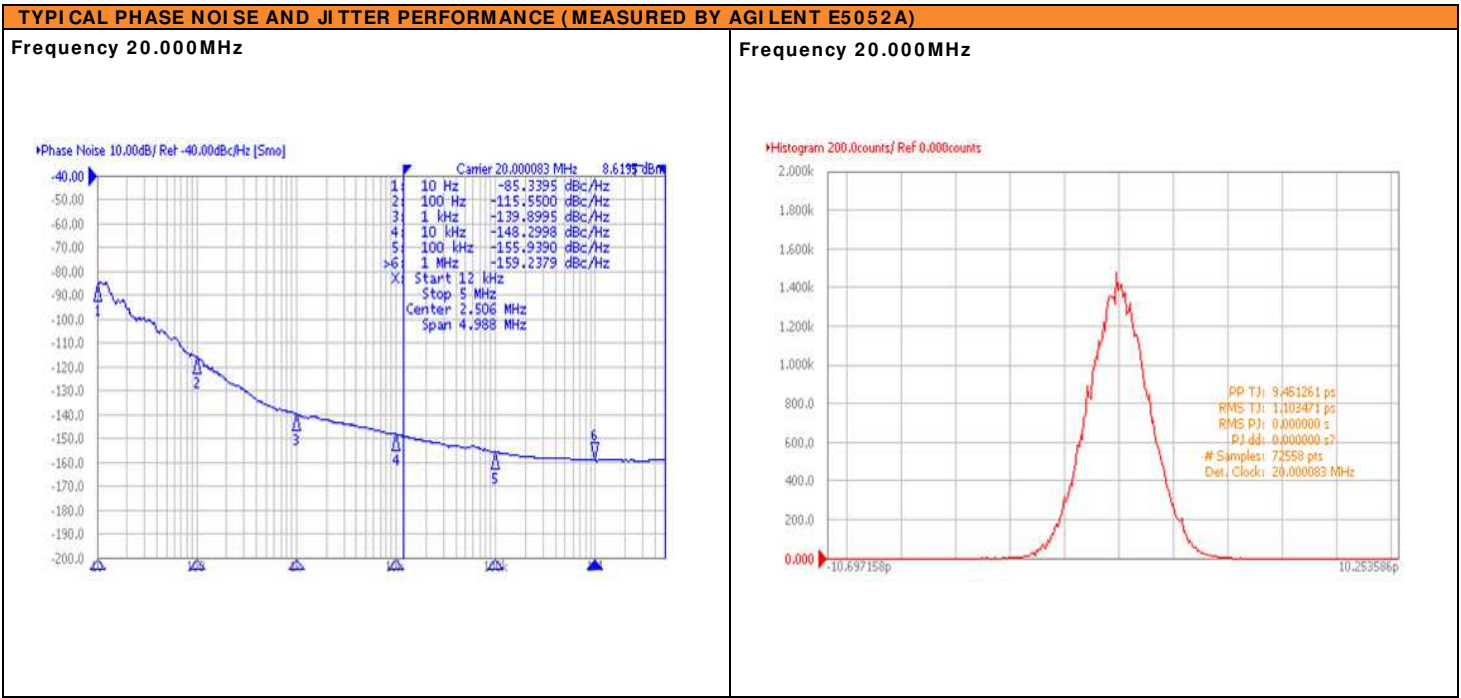
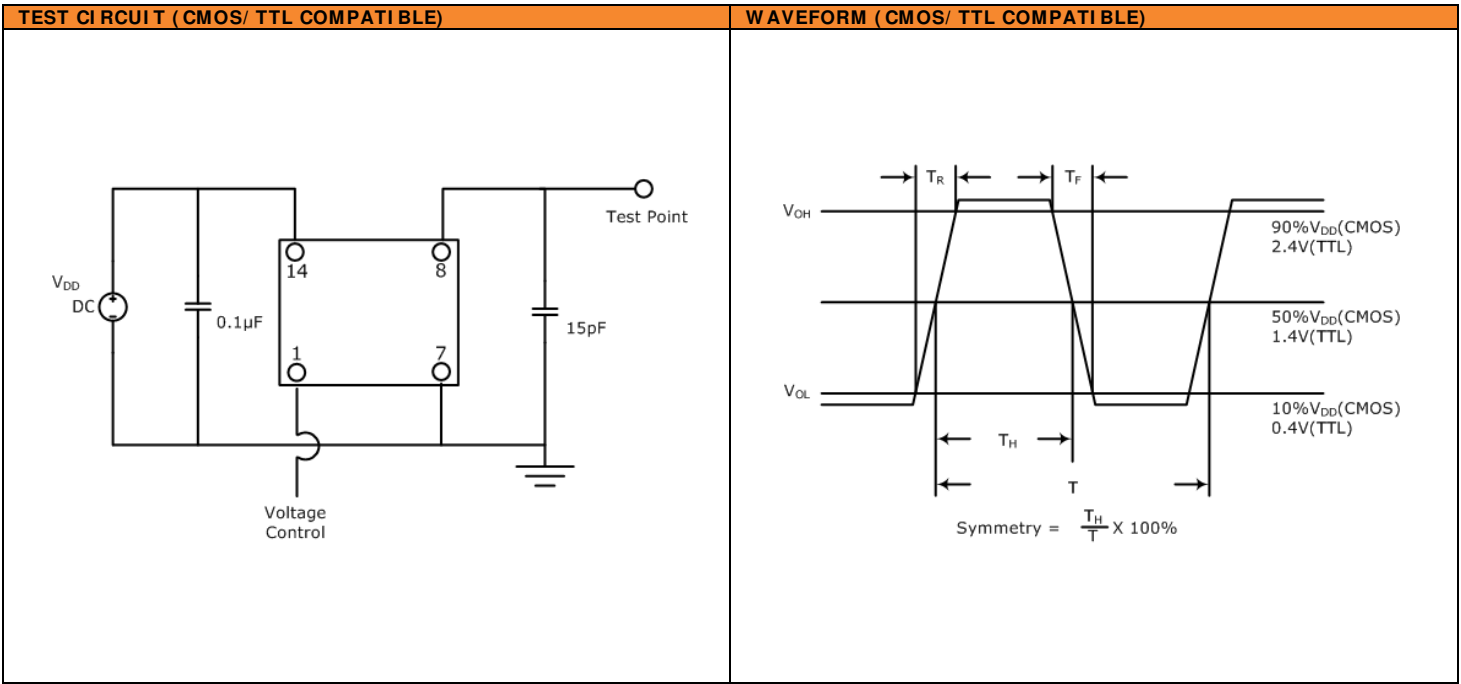
Cage Code: 4GUT4
 To customize your parameters contact a Suntsu representative.
 * For frequency stability option D contact a Suntsu representative.

ELECTRICAL PARAMETERS	UNITS	MIN.	TYP.	MAX.	REMARKS
Frequency Range	MHz	1		160	
Frequency Stability (Includes Initial Tolerance at 25°C, Frequency Stability over Operating Temperature, Output Load Change, Supply Voltage Change, and First Year Aging at 25°C.)	ppm	-20		+20	See part numbering guide for options.
Operating Temperature	°C	-40		+85	See part numbering guide for options.
Storage Temperature		-55		+125	
Supply Voltage (V _{DD})	3.3V Option	3.135	3.3	3.465	Available with AT-Cut Fundamental and PLL.
	5.0V Option	4.750	5.0	5.250	Only available with AT-Cut Fundamental.
Current (I _{DD})	3.3V Option			40	
	5.0V Option			50	
Control Voltage (V _C)	3.3V Option	0.3		3.0	
	5.0V Option	0.5		4.5	
Pullability	ppm	±50	±100	±150	See part numbering guide for options.
Linearity	%			10	
Output Load	CMOS			15	
	TTL			10	
Output Logic Levels	CMOS Output Logic High (V _{OH})	0.9* V _{DD}			
	CMOS Output Logic Low (V _{OL})			0.1* V _{DD}	
	TTL Output Logic High (V _{OH})	2.4			
	TTL Output Logic Low (V _{OL})			0.4	
Rise (T _R) and Fall (T _F) Time	ns			5	
Symmetry (Duty Cycle)	%	45	50	55	
Start-Up Time	ms			10	
Phase Jitter (12kHz ~ 20MHz)	ps			1	AT-Cut Fundamental.
				5	PLL (Phase Lock Loop).

OUTLINE DRAWING

PIN	FUNCTION
1	VOLTAGE CONTROL
7	GND
8	OUTPUT
14	V _{DD}

NOTE: Dimensions in millimeters (mm).



ENVIRONMENTAL & MECHANICAL SPECIFICATIONS	MARKING
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003

Frequency in MHz

Line 1: X X . X X X

Line 2: S F Y W W

Suntsu Manufacturing Identifier Week Year