

FEATURES	APPLICATIONS
<ul style="list-style-type: none"> <li>±20ppm (Frequency Stability) Available</li> <li>Standard Full-Size Package</li> <li>CMOS/TTL Compatible</li> <li>RoHS Compliant</li> <li>Fundamental or PLL (Phase Lock Loop) Available</li> </ul>	<ul style="list-style-type: none"> <li>Phase Locked Loops Circuit</li> <li>Synthesizers</li> <li>Base Stations</li> </ul>



**PART NUMBERING GUIDE**

**SUNTSU VCXO** → **SVC HS C 3 A 48 A - 40.000M** ← **FREQUENCY (MHz)**

<b>FULL SIZE</b>	→	SVC	HS	C	3	A	48	A	-	40.000M	←	<b>FREQUENCY (MHz)</b>
<b>CMOS</b>	→	C										
<b>SUPPLY VOLTAGE</b>	→				3							
3: 3.3V±5%												
5: 5.0V±5%												
<b>FREQUENCY STABILITY</b>	→					A	48	A				
A: ±50ppm												
B: ±30ppm												
C: ±25ppm												
*D: ±20ppm												
												<b>PULLABILITY</b>
												A: ±150ppm
												B: ±100ppm
												C: ±50ppm
												<b>OPERATING TEMPERATURE RANGE</b>
												07: 0°C to +70°C
												16: -10°C to +60°C
												17: -10°C to +70°C
												27: -20°C to +70°C
												38: -30°C to +85°C
												48: -40°C to +85°C

Cage Code: 4GUT4  
 To customize your parameters contact a Suntsu representative.  
 \* For frequency stability option D contact a Suntsu representative.

ELECTRICAL PARAMETERS	UNITS	MIN.	TYP.	MAX.	REMARKS
Frequency Range	MHz	1		160	
Frequency Stability (Includes Initial Tolerance at 25°C, Frequency Stability over Operating Temperature, Output Load Change, Supply Voltage Change, and First Year Aging at 25°C.)	ppm	-20		+20	See part numbering guide for options.
Operating Temperature	°C	-40		+85	See part numbering guide for options.
Storage Temperature		-55		+125	
Supply Voltage (V <sub>DD</sub> )	3.3V Option	3.135	3.3	3.465	Available with AT-Cut Fundamental and PLL.
	5.0V Option	4.750	5.0	5.250	Only available with AT-Cut Fundamental.
Current (I <sub>DD</sub> )	3.3V Option			40	
	5.0V Option			50	
Control Voltage (V <sub>C</sub> )	3.3V Option	0.3		3.0	
	5.0V Option	0.5		4.5	
Pullability	ppm	±50	±100	±150	See part numbering guide for options.
Linearity	%			10	
Output Load	CMOS			15	
	TTL			10	
Output Logic Levels	CMOS Output Logic High (V <sub>OH</sub> )	0.9* V <sub>DD</sub>			
	CMOS Output Logic Low (V <sub>OL</sub> )			0.1* V <sub>DD</sub>	
	TTL Output Logic High (V <sub>OH</sub> )	2.4			
	TTL Output Logic Low (V <sub>OL</sub> )			0.4	
Rise (T <sub>R</sub> ) and Fall (T <sub>F</sub> ) Time	ns			5	
Symmetry (Duty Cycle)	%	45	50	55	
Start-Up Time	ms			10	
Phase Jitter (12kHz ~ 20MHz)	ps			1	AT-Cut Fundamental.
				5	PLL (Phase Lock Loop).

**OUTLINE DRAWING**

PIN	FUNCTION
1	VOLTAGE CONTROL
4	GND
5	OUTPUT
8	V <sub>DD</sub>

NOTE: Dimensions in millimeters (mm).

